

**IN THE SPECIFICATION**

Please insert the following paragraph starting on page 1, line 5:

**CROSS-REFERENCE TO RELATED APPLICATION AND CLAIM OF PRIORITY**

The present application is a continuation of co-pending U.S. Patent Application Serial No. 09/666,208, filed on September 21, 2000, entitled "Chip Testing Within A Multi-Chip Semiconductor Package," which is assigned to the present assignee and hereby incorporated by reference in its entirety.